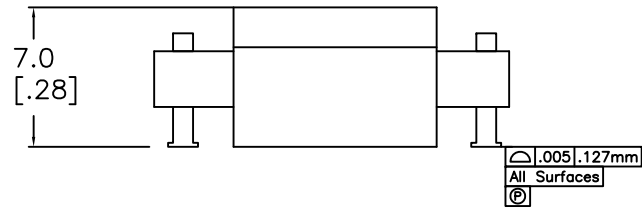
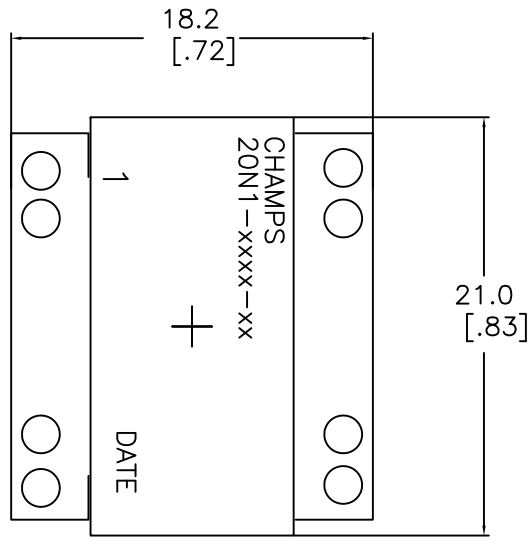
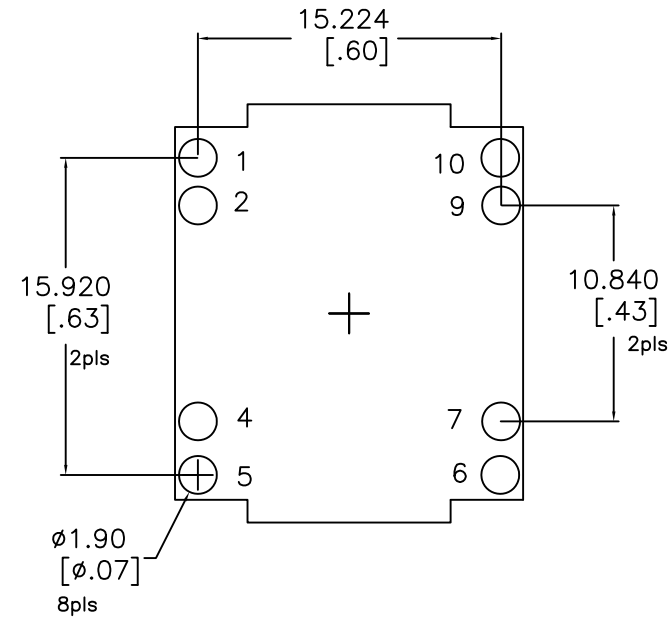


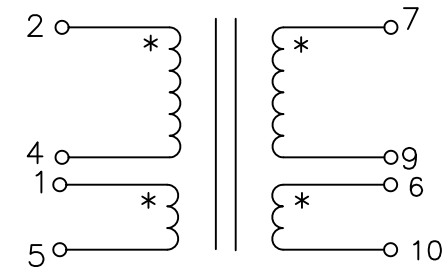
MECHANICAL DIMENSIONS [TOP VIEW]



SUGGESTED PAD LAYOUT [TOP VIEW]



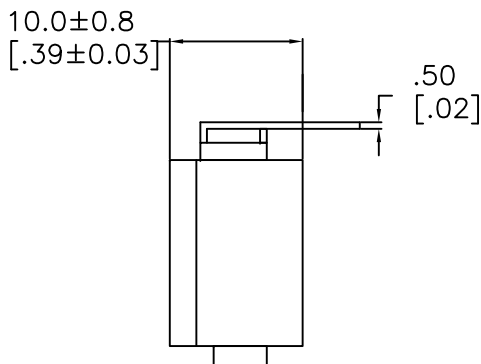
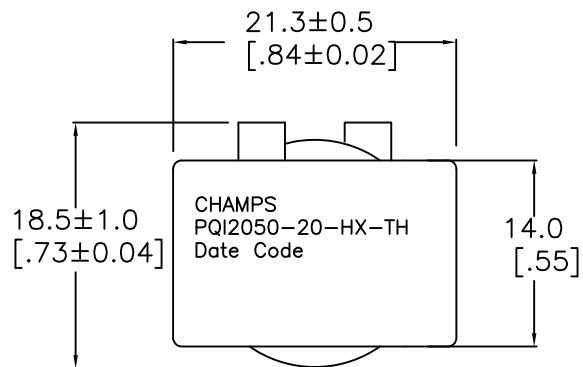
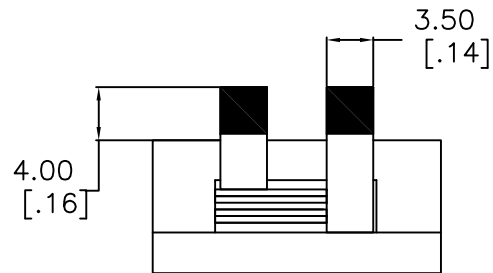
Schematic



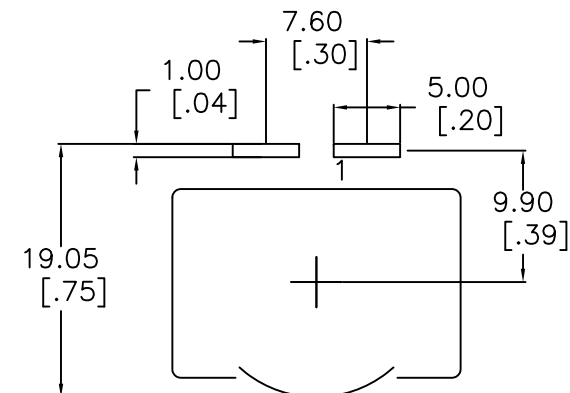
NOTES:

1. TURNS RATIO [7-9] : [2-4] = 0.50 $\pm$ 2%
TURNS RATIO [1-5] : [2-4] = xx $\pm$ 2% || [6-10] : [2-4] = xx $\pm$ 2%
2. DCR [2-4] = 41 mohm Nom, [7-9] = 12.0 mohm Nom. [1-5] = 300 mohm Nom.
3. Inductance [2-4]= 278 uH $\pm$ 25%, 100KHz, 1.0 VRMS @ 25C
4. Leakage Inductance [2-4] Short [7-9] = 150 nH Nom @100 KHz
5. Dielectric Strength [2-4], [1-5] to [7-9] & Core 1500 VDC Nom [Capable 2250Vdc] [7-9], [6-10] to CORE 500 VDC
6. RoHS & REACH Compliant
7. Rated Temperature Range: -55C to +130C [Inclusive of Temp Rise]

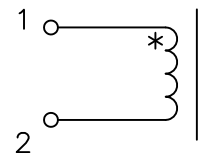
No.	DESCRIPTION	REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION				
CHAMPS TECHNOLOGIES				
TOLERANCES/UNITS in MM UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs-Tech PN 20N1-1005
XXX $\pm$ 0.254	DRAWN	HE	10.24.19	Customer
XX $\pm$ 0.508	CHKD			Part #:
X $\pm$ 0.78	APPR			SIZE
ANGLE $\pm$				SCALE 3:1
				ISSUE A
				REV 00



SUGGESTED THRU-HOLE LAYOUT



Schematic



ELECTRICAL INFO:

INDUCTANCE [1-2] = 20uH Nom, 18 Min. @100kHz/1.0V 6Adc

INDUCTANCE [1-2] = 17 uH Min. @100kHz/1.0V 6Adc

DCR [1-2] = 3.6 mohms Nom || 4.5Max

DIELECTRIC ISOLATION > 500 VDC [1-2] : CORE

SATURATION CURRENT @25C = 7Adc | @100C = 6.4Adc

HEATING CURRENT FOR 40C RISE AT 25C AMBIENT = 22 Adc

Operating Temp Range -55C to +130C [Inclusive of Temp Rise]

No.	DESCRIPTION	REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION				
CHAMPS TECHNOLOGIES				
TOLERANCES/UNITS in MM UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs-Tech PN PQI2050-20HX-TH-10m
.XXX ± .25	DRAWN	DK	11.19.18	Customer
.XX ± .51	CHKD			Part #:
.X ANGLE ± 1.0	APPR	HE		INDUCTOR
		SIZE	SCALE 2:1	ISSUE A
				REV 00