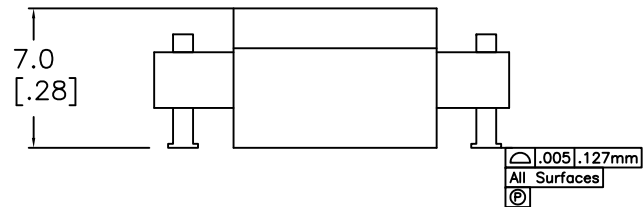
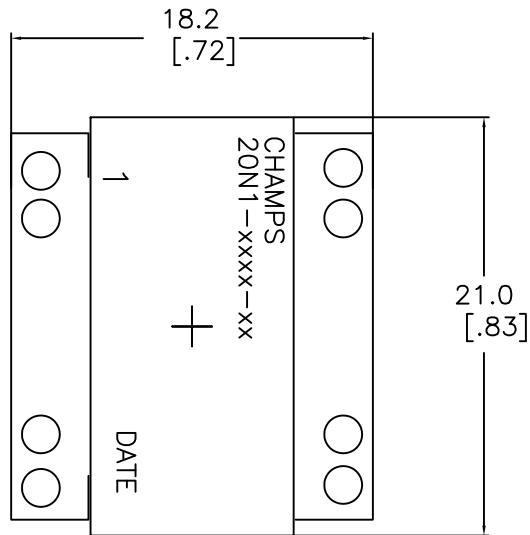
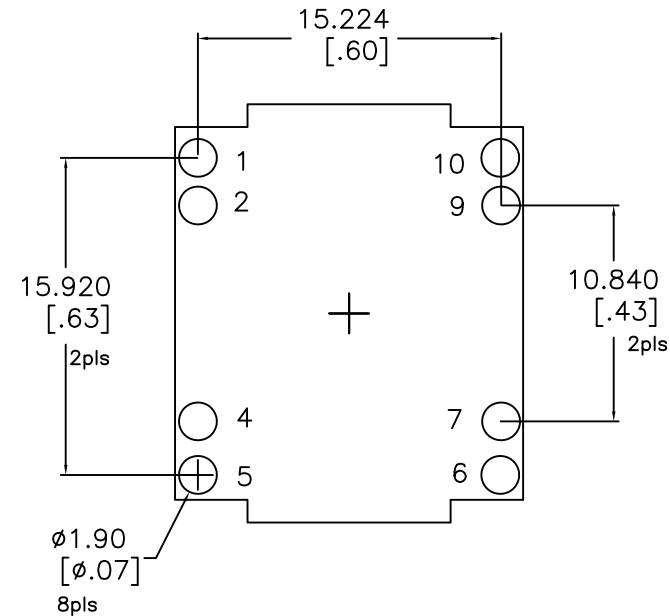


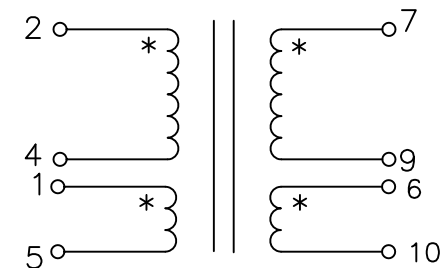
MECHANICAL DIMENSIONS [TOP VIEW]



SUGGESTED PAD LAYOUT [TOP VIEW]



Schematic

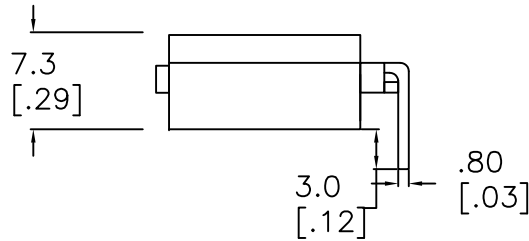
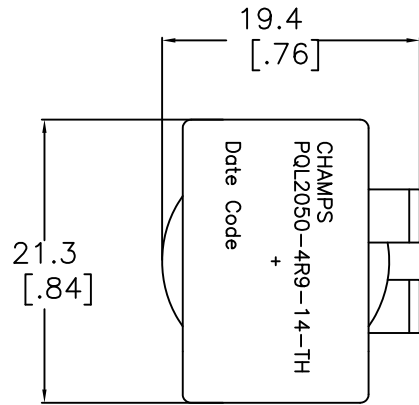


NOTES:

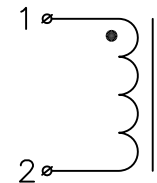
1. TURNS RATIO [7-9] : [2-4] = 1.0 $\pm$ 2%
TURNS RATIO [1-5] : [2-4] = xx $\pm$ 2% || [6-10] : [2-4] = xx $\pm$ 2%
2. DCR [2-4] = 3.5 mohm Nom, [7-9] = 3.5 mohm Nom. [1-5] = 300 mohm Nom.
3. Inductance [2-4]= 25 uH $\pm$ 25%, 100KHz, 1.0 VRMS @ 25C
4. Leakage Inductance [2-4] Short [7-9] = 35 nH Nom @100 KHz
5. Dielectric Strength [2-4], [1-5] to [7-9] & Core 1500 VDC Nom [Capable 2250Vdc] [7-9], [6-10] to CORE 500 VDC
6. RoHS & REACH Compliant
7. Rated Temperature Range: -55C to +130C [Inclusive of Temp Rise]

No.	DESCRIPTION	REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION				
CHAMPS TECHNOLOGIES				
TOLERANCES/UNITS in MM UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs-Tech PN 20N1-0303
XXX $\pm$ 0.254	DRAWN	HE	10.24.19	Customer
XX $\pm$ 0.508	CHKD			Part #:
X $\pm$ 0.78	APPR			SIZE
ANGLE $\pm$				SCALE 3:1
				ISSUE A
				REV 00

SUGGESTED THRU-HOLE LAYOUT



Schematic



NOTES:

1. INDUCTANCE [1-2] = 4.9 μ H $\pm$ 10% @100kHz 1.0V @ 0Adc
2. INDUCTANCE [1-2] = Within Range $\pm$ 10% Measured Value at 0Adc @Irated 14Adc
3. DCR [1-2] = 2.2 mohms Nom.
4. DIELECTRIC ISOLATION > 500 VDC [1-2] : CORE
5. SATURATION CURRENT @25C = 16Adc | @100C = 15Adc
6. HEATING CURRENT FOR 45C RISE AT 25C AMBIENT = 32 Adc
7. Operating Ambient Temperature: -55C to +130C
8. RoHS Level 6/6, REACH & CMRT Compliant || 96/4 Sn/Ag Pin Composition

No.	DESCRIPTION	REVISIONS	DATE	APPR
THIRD ANGLE PROJECTION				
CHAMPS TECHNOLOGIES				
TOLERANCES/UNITS in MM UNLESS OTHERWISE INDICATED		SIGN	DATE	Champs-Tech PN PQL2050-4R9-14-TH
.XXX $\pm$ 0.254	DRAWN	HE	01.15.18	Customer
.XX $\pm$ 0.38	CHKD			Part #:
.X $\pm$ 0.78	APPR			SIZE
ANGLE $\pm$				SCALE 2:1
				ISSUE A
				REV 00